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APPLICATION NO). F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/748,667		12/31/2003	Chao-Cheng Lee	TOP 348 2788	
23995	7590	05/19/2005		EXAMINER	
RABIN 8	Berdo, PO		NGUYEN, PATRICIA T		
1101 14TF	I STREET,	NW			
SUITE 50	0		ART UNIT	PAPER NUMBER	
WASHINGTON DC 20005				2817	

DATE MAILED: 05/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		10/748,667	LEE ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Patricia T. Nguyen	2817				
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1) 🗌	Responsive to communication(s) filed on						
2a) <u></u> □	This action is FINAL . 2b)⊠ This action is non-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims	٠					
4) 🖂	Claim(s) <u>1-13</u> is/are pending in the application.						
,_	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠	Claim(s) 4-9 is/are allowed.						
6)⊠	⊠ Claim(s) <u>1-3 and 10-13</u> is/are rejected.						
7)	Claim(s) is/are objected to.		•				
8)□	Claim(s) are subject to restriction and/or	r election requirement.					
Applicat	ion Papers		,				
9)[]	The specification is objected to by the Examine	r.					
10)	10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority (ınder 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
· ·	a)⊠ All b)□ Some * c)□ None of:						
1.⊠ Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachmen	t(s)	·					
	e of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) Notic	e of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ite				
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	6) Other:	atent Application (PTO-152)				

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 – 3 and 10 –13 are rejected under 35 U.S.C. 102(b) as being anticipated by Schlotterer et al., U.S. Patent # 5,525,985.

Figs. 1 and 3 of Schlotterer et al. discloses an amplifier circuit comprising: amplifier 80 can be read as an operational amplifier; resistor network 84 can be read as a resistor network wherein node connects between the two horizontal resistors R from the output of amplifier 80 can be read as an input node; node connects between the next two horizontal resistors R and the resistor 2R can be read as an output node; resistor 2R connects to the input node and ground can be read as a first resistor; the horizontal resistor connects between input node and the output node can be read as a second resistor.

⁽e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

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Claims 1 and 10 are rejected under 35 U.S.C. 102(e) as being anticipated by Aswell, U.S. Patent # 6,703,682 B2.

Figs. 4 and 21 of Aswell discloses an amplifier circuit comprising: amplifier 62 can be read as an operational amplifier; MOS feedback resistor 52 can be read as a resistor network wherein in Fig. 21, node N3 can be read as an input node; node N2 can be read as an output node; MOS resistor MN3 can be read as a first resistor; resistor RP2 can be read as a second resistor.

Regarding claim 10, see spec. col. 4, lines 52-62.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2, 3, 11-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aswell, U.S. Patent # 6,703,682 B2.

Although Aswell does not mention that the resistance of the first resistor is two times larger than the resistance of the second resistor or the equivalent resistance of the resistor network is 2nx R, wherein the resistor network includes n stages and the resistance of the second resistor is R, it would have been obvious at the time the invention was made to a person having ordinary skill in the art to design the resistances of the first and second resistors as claimed since the values of the resistances are

design variables (see col. 4, lines 48-54 of Aswell) in order to meet system requirements.

Allowable Subject Matter

Claims 4-9 are allowed.

The following is a statement of reasons for the indication of allowable subject matter: Prior art does not teach nor render obvious a circuit that has, in combination with other limitations, a first resistor network comprising a plurality of stages connected serially, coupled to the converting input terminal for receiving an input voltage, wherein each stage of the first resistor network comprises: an input node; an output node; a first resistor coupled between the input node and the ground; and a second resistor coupled between the input node and the output node.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. Patents # 3,863,201, # 4,484,295, # 4,037,170, and # 5,231,627 contain some limitations of the claimed invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T. Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-309-4940. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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PTN May 12, 2005

PATRICIA NGUYEN
PRIMARY EXAMINER